

Fig.1

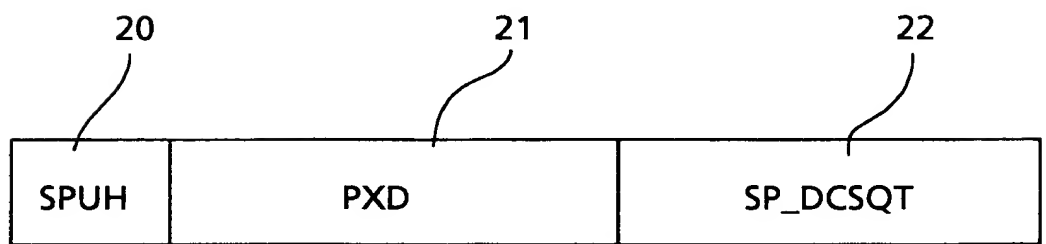
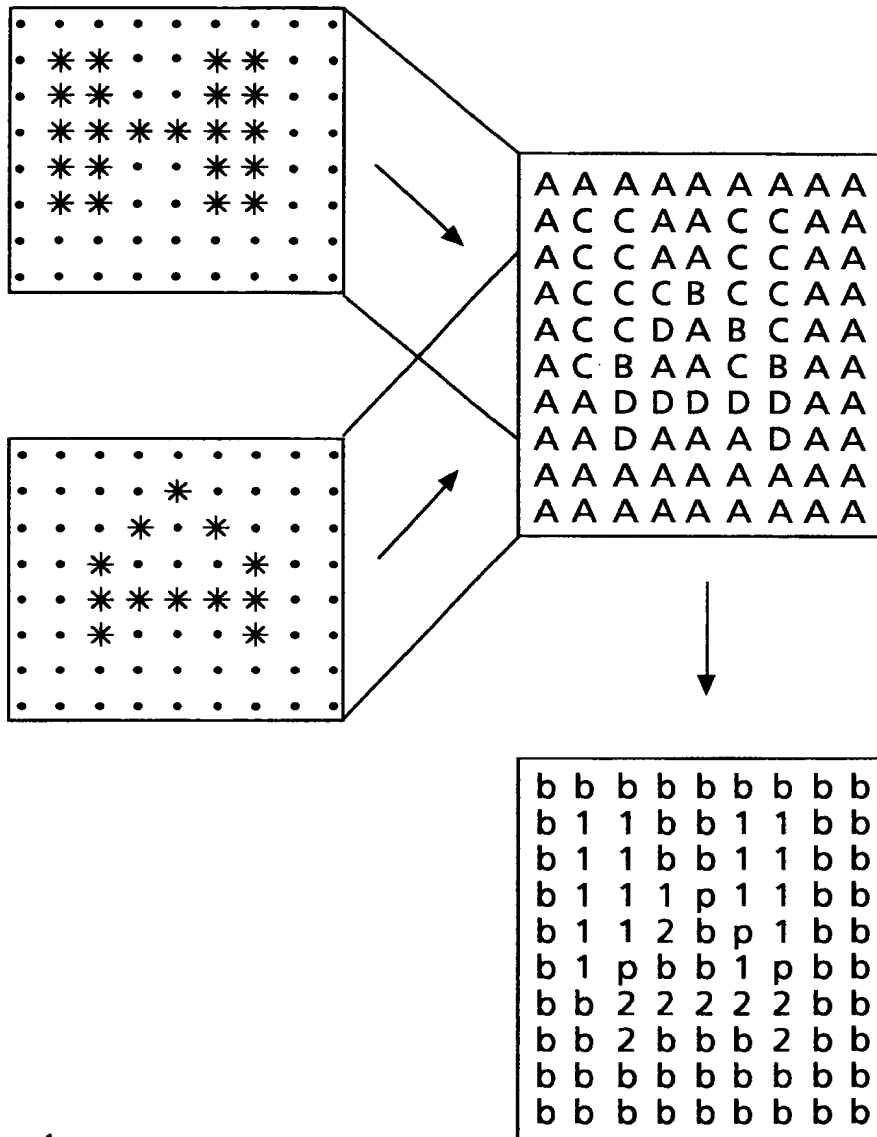
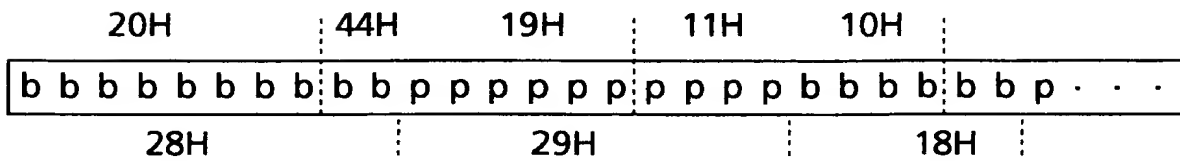


Fig.2

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**Fig.3**



**Fig.4**

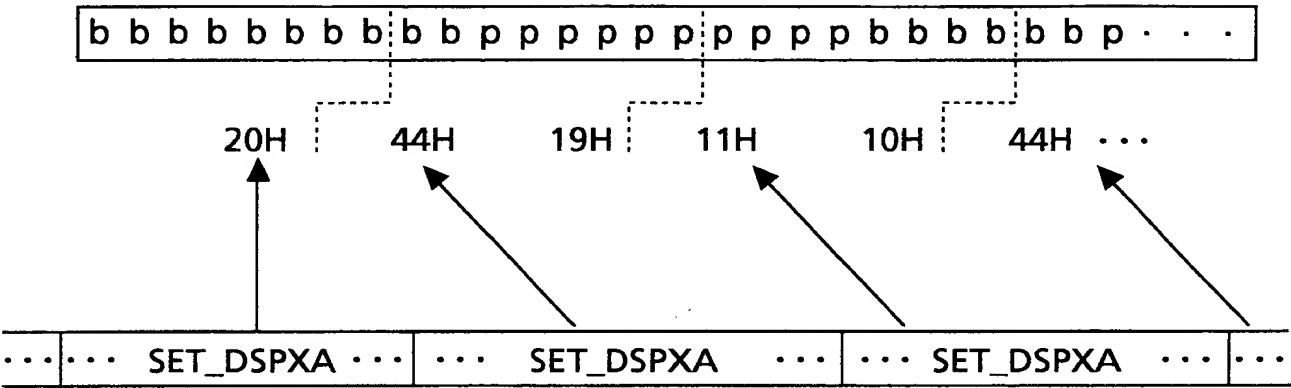


Fig.5

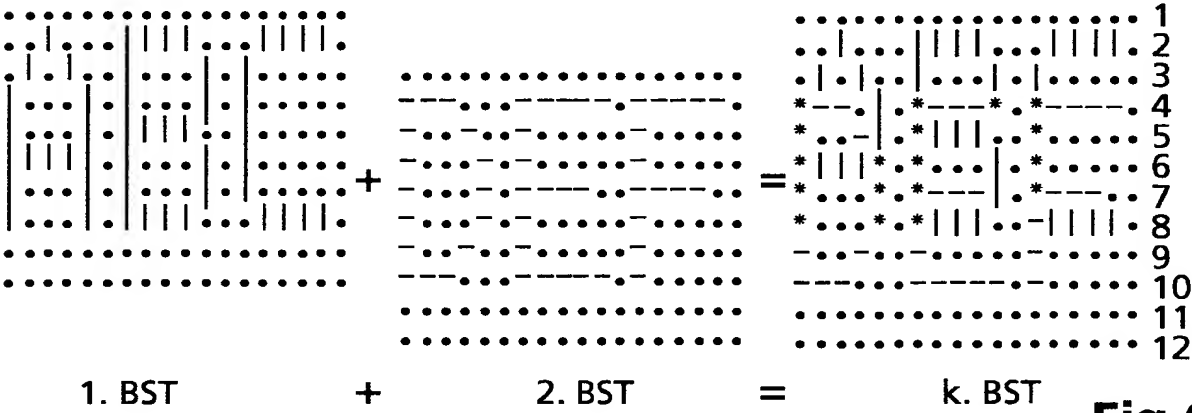


Fig.6

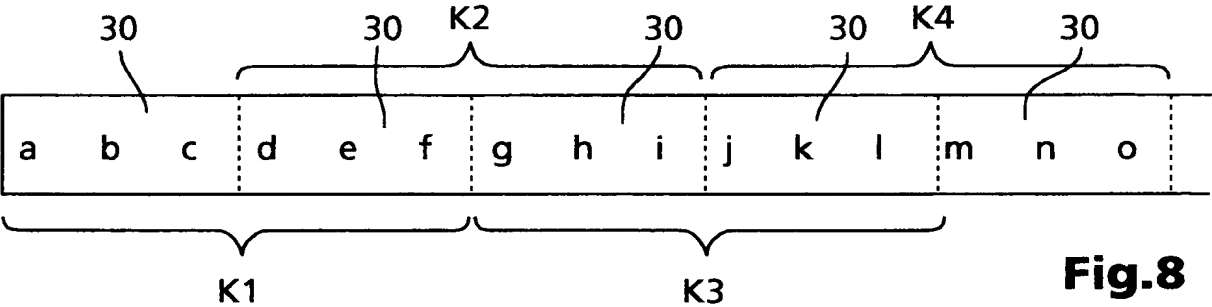


Fig.8

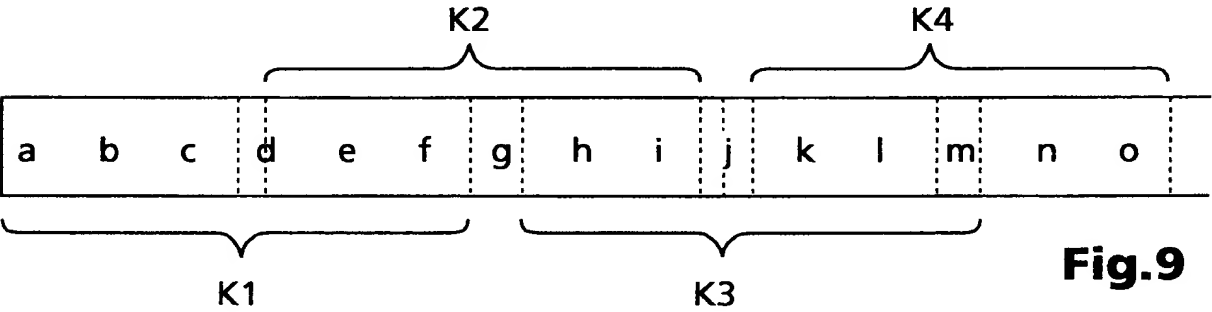


Fig.9

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0000: ##### SPUH:
0000: 01 FC # SPDSZ = 508 = 0x1FC
0002: 00 FE # SP_DCSQTA = 254 = 0xFE

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0004: ##### PXD:
0004: 44 44 44 44 44 44 44 44 44 46 46 44 64 44 64 64
0014: 44 44 74 45 64 76 66 44 74 44 44 74 44 74 75 55
0024: 64 75 55 44 54 45 44 54 44 44 54 44 44 00 00 44
0034: 64 44 66 66 44 46 66 64 75 54 64 75 55 74 75 55
0044: 54 76 66 74 74 44 64 74 44 44 74 44 74 76 66 44
0054: 56 66 64 55 54 44 55 55 54 54 44 44 00 00 00 00
0064: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0074: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0084: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0094: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00A4: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00B4: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00C4: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00D4: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00E4: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00F4: 00 00 00 00 00 00 00 00 00 00 00

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00FE: ##### SP_DCSQT:
00FE: ##### SP_DCSQ 0 = 0x0
00FE: 00 00 # SP_DCSQ_STM = 0s => start frame: 0
0100: 01 16 # SP_NXT_DCSQ_SA = 278 = 0x116
0102: 06 00 04 00 33 # SET_DSPXA uf=0x4 bf=0x33
0107: 03 11 00 # SET_COLOR b=0 p=0 e1=1 e2=1
010A: 04 FF FF # SET_CONTR (0..15) b=15 p=15 e1=15 e2=15
010D: 05 06 40 75 09 60 9F # SET_DAREA sx=100 ex=117 sy=150 ey=159
0114: 01 # STA_DSP
0115: FF # CMD_END
0116: ##### SP_DCSQ 1 = 0x1
0116: 00 57 # SP_DCSQ_STM => start frame: 30
0118: 01 31 # SP_NXT_DCSQ_SA = 305 = 0x131
011A: 07 00 10 # CHG_COLCON (Extended field size = 16 = 0x10)
011D: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
0121: 00 74 10 10 FF FF # PX_CTLI 0: cspn=116 col= 0 1 0 1 cntr=F F F F
0127: 0F FF FF FF # LN_CTLI Termination Code
012B: 06 00 05 00 34 # SET_DSPXA uf=0x5 bf=0x34
0130: FF # CMD_END
0131: ##### SP_DCSQ 2 = 0x2
0131: 00 AF # SP_DCSQ_STM => start frame: 60
0133: 01 4C # SP_NXT_DCSQ_SA = 332 = 0x14C
0135: 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
0138: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
013C: 00 72 10 10 FF FF # PX_CTLI 0: cspn=114 col= 0 1 0 1 cntr=F F F F
0142: 0F FF FF FF # LN_CTLI Termination Code
0146: 06 00 06 00 35 # SET_DSPXA uf=0x6 bf=0x35
014B: FF # CMD_END
014C: ##### SP_DCSQ 3 = 0x3
014C: 01 07 # SP_DCSQ_STM => start frame: 90
014E: 01 67 # SP_NXT_DCSQ_SA = 359 = 0x167
0150: 07 00 10 # CHG_COLCON (Extended field size = 16 = 0x10)
0153: 00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
0157: 00 70 10 10 FF FF # PX_CTLI 0: cspn=112 col= 0 1 0 1 cntr=F F F F
015D: 0F FF FF FF # LN_CTLI Termination Code
0161: 06 00 07 00 36 # SET_DSPXA uf=0x7 bf=0x36
0166: FF # CMD_END

```

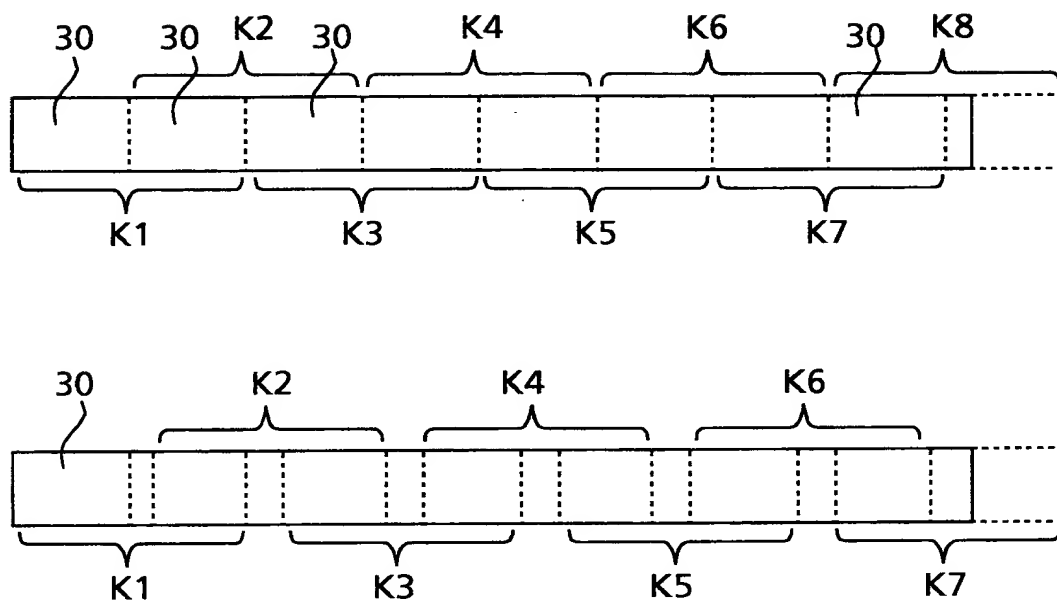
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0167:##### SP_DCSQ 4 = 0x4
0167:01 5F # SP_DCSQ_STM => start frame: 120
0169:01 82 # SP_NXT_DCSQ_SA = 386 = 0x182
016B 07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
016E:00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
0172:00 6E 10 10 FF FF # PX_CTLI 0: cspn=110 col= 0 1 0 1 cntr=F F F F
0178:0F FF FF FF # LN_CTLI Termination Code
017C:06 00 08 00 37 # SET_DSPXA uf=0x8 bf=0x37
0181:FF # CMD_END
0182:##### SP_DCSQ 5 = 0x5
0182:01 B7 # SP_DCSQ_STM => start frame: 150
0184:01 9D # SP_NXT_DCSQ_SA = 413 = 0x19D
0186:07 00 10 # CHG_COLCON (Extended field size = 16 = 0x10)
0189:00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
018D:00 6C 10 10 FF FF # PX_CTLI 0: cspn=108 col= 0 1 0 1 cntr=F F F F

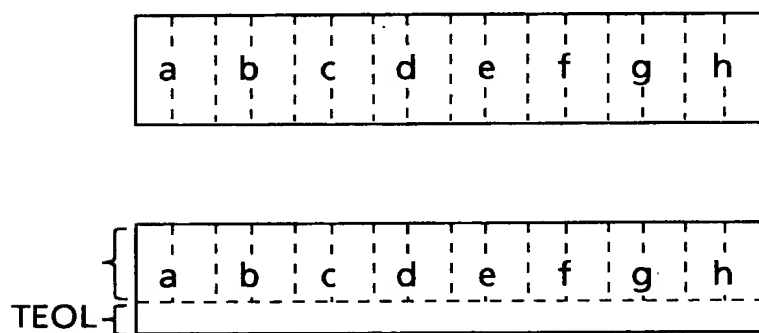
0193:0F FF FF FF # LN_CTLI Termination Code
0197:06 00 09 00 38 # SET_DSPXA uf=0x9 bf=0x38
019C:FF # CMD_END
019D:##### SP_DCSQ 6 = 0x6
019D:02 0F # SP_DCSQ_STM => start frame: 180
019F 01 B8 # SP_NXT_DCSQ_SA = 440 = 0x1B8
01A1:07 00 10 # CHG_COLCON (Extended field size = 16 = 0x10)
01A4:00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
01A8:00 6A 10 10 FF FF # PX_CTLI 0: cspn=106 col= 0 1 0 1 cntr=F F F F
01AE:0F FF FF FF # LN_CTLI Termination Code
01B2:06 00 0A 00 39 # SET_DSPXA uf=0xA bf=0x39
01B7:FF # CMD_END
01B8:##### SP_DCSQ 7 = 0x7
01B8:02 67 # SP_DCSQ_STM => start frame: 210
01BA:01 D3 # SP_NXT_DCSQ_SA = 467 = 0x1D3
01BC:07 00 10 # CHG_COLON (Extended field size = 16 = 0x10)
01BF:00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
01C3:00 68 10 10 FF FF # PX_CTLI 0: cspn=104 col= 0 1 0 1 cntr=F F F F
01C9:0F FF FF FF # LN_CTLI Termination Code
01CD:06 00 0B 00 3A # SET_DSPXA uf=0xB bf=0x3A
01D2:FF # CMD_END
01D3:##### SP_DCSQ 8 = 0x8
01D3:02 BF # SP_DCSQ_STM => start frame: 240
01D5:01 EE # SP_NXT_DCSQ_SA = 494 = 0x1EE
01D7:07 00 10 # CHG_COLCON (Extended field size = 16 = 0x10)
01DA:00 02 10 C8 # LN_CTLI 0: n=1 csln=2 ctln=200
01DE:00 66 10 10 FF FF # PX_CTLI 0: cspn=102 col= 0 1 0 1 cntr=F F F F
01E4:0F FF FF FF # LN_CTLI Termination Code
01E8:06 00 0C 00 3B # SET_DSPXA uf=0xC bf=0x3B
01ED:FF # CMD_END
01EE:##### SP_DCSQ 9 = 0x9
01EE:03 17 # SP_DCSQ_STM => start frame: 270
01F0:01 EE # SP_NXT_DCSQ_SA = 494 = 0x1EE
01F2:07 00 06 # CHG_COLON (Extended field size = 6 = 0x6)
01F5:0F FF FF FF # LN_CTLI Termination Code
01F9:02 # STP_DSP
01FA:FF # CMD_END
01FB:FF # Stuffing Byte => even SP size

```

Fig.7



**Fig.10**



**Fig.11**